Search Notes	Application/Control No.	Applicant(s)/Patent Under Reexamination
	10560920	JEONG, SEONG IK.
	Examiner	Art Unit
	YUK CHOW	2629

SEARCHED

Class	Subclass	Date	Examiner
345	55, 100	9/28/2010	YC
365	189, 230, 233.12	9/28/2010	YC

SEARCH NOTES

Search Notes	Date	Examiner
Display memory, addressing column in groups to reduce peak current.	9/28/2010	YC
Maintain rejection: cited ref, latch circuit read on claimed limitaion: transfer gates, since they have same function, regardless of the different structure.	4/23/2011	YC
Indepedent claims include second signal for switching second transfer gates, to allow data to be transfer red from memory to buffer through the second transfer gates. This is not taught by cited reference.	8/12/2011	YC
Inventors search conducted, no double patenting issue.	8/12/2011	YC

INTERFERENCE SEARCH

Class	Subclass	Date	Examiner
Same as above	same as above	8/12/2011	YC